REMARKS

Claims 9, 10, 12-19 and 34-40, as amended, remain herein. Claims 1-8, 11 and 20-33 are canceled without prejudice or disclaimer.

1.   Claims 15, 16 and 19 were rejected under 35 U.S.C. § 102 (b) over Ochiai '973.

Ochiai '973 fails to disclose or suggest "the arbitration circuit" instructing "the command generation block to provide a *wait cycle* when a memory access request is made by the bank access data alone from the block permitted to access the memory," as recited in claim 15. An idle state of the SDRAM is not a "wait cycle" as recited in claim 15. (see Ochiai '973, Fig. 28; col. 1, lines 41- 64).

Further, Ochiai fails to disclose or suggest "block access data...comprising two sets of the bank access data belonging to different banks," as recited in claim 15. The data block in Ochiai fails to describe the "block access data" recited in claim 15. (see Ochiai '973, col. 25, line 51-57).

Furthermore, Ochiai fails to disclose or suggest "when a memory access request is made by the bank access data alone from the block permitted to access the memory." Ochiai is simply silent regarding this feature recited in claim 15.

For all the foregoing reasons, there is no disclosure or teaching in Ochiai '973 of all elements of applicants' presently claimed invention. Accordingly, Ochiai '973 is not an adequate basis for a rejection of applicants' claims under § 102. Nor is there any disclosure in Ochiai '973 that would have suggested applicants' claimed invention to one of ordinary skill in

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this art. Reconsideration and withdrawal of this rejection of claim 15 is respectfully requested. Claims 16 and 19 are patentable under the same rationale.

2.   Claims 17 and 18 are rejected under 35 U.S.C. 103 (a) over Ochiai '973. Claims 17 and 18 are patentable for the reasons discussed above with respect to claim 15.

3.   Claims 9, 10, 12-14, 34-40 are rejected under 35 U.S.C. § 103 (a) over Ochiai '973 and Miyawaki '266.

Claim 9 recites, *inter alia*, "bank access data… comprising a predetermined number of bytes for performing writing or reading on a same bank of the memory" and "block access data… comprising two sets of the bank access data belonging to different banks." Neither Ochiai nor Miyawaki teaches or suggests the recited elements of claim 9. The data block in Ochiai fails to describe the "block access data" recited in claim 9. See Ochiai, col. 25, line 51-57. Miyawaki likewise fails to make up for the deficiency of Ochiai.

Further, neither Ochiai '973 nor Miyawaki '266 teaches or suggests "when a second-half bank where memory access is permitted immediately before is the same as the first-half bank of a subsequent memory access request, the arbitration circuit changes an order of memory access of the bank access data in the block access data," as recited in claim 9.

The Office Action admits that Ochiai '973 fails to describe changing a priority of memory access; however it alleges that Miyawaki '266 makes up for such deficiency. While Miyawaki '266 describes a method of controlling memory access operations by changing respective priorities of the operations, there is no disclosure in Miyawaki '266 that such change

of priority takes place "when a second-half bank where memory access is permitted immediately before is the same as the first-half bank of a subsequent memory access request," as recited in claim 9.
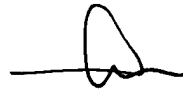
With respect to claim 34, neither Ochiai '973 nor Miyawaki '266 teaches or suggests "wherein the arbitration circuit designates an arbitrating method for changing priority of memory access from the plurality of blocks when the memory access request from the plurality of blocks is made to the same bank as immediately preceding access," as recited in claim 34.

Thus, there is no disclosure or teaching in either Ochiai '973 or Miyawaki '266 of all elements of applicants' claimed invention. Nor is there any disclosure or teaching in either Ochiai '973 or Miyawaki '266 that would have suggested applicants' claimed invention to one of ordinary skill in the art. Still further, there is no disclosure or teaching in either of these references, and no sound basis stated in this record, that would have suggested the desirability of combining any portions thereof effectively to anticipate or render obvious applicant's claimed invention. Accordingly, reconsideration and withdrawal of these grounds of rejection, and allowance of all claims 9, 10, 13, 14, and 34-40 are respectfully requested.

Accordingly, the application is now fully in condition for allowance and a notice to that effect is respectfully requested. The PTO is hereby authorized to charge/credit any fee deficiencies or overpayments to Deposit Account No. 19-4293 (Order No. 28951.5406). If further amendments would place this application in even better condition for issue, the Examiner is invited to call applicant's undersigned attorney at the number listed below.

Respectfully submitted,

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